AMENDMENTS TO THE SPECIFICATION

Please add the following new paragraph after paragraph [1030]:

[1030.1] In general, a processor that includes an automatic prefetch facility may be made by defining thereon, during fabrication of an integrated circuit, (i) at least one writable store of the processor suitable for delimiting a subset of addressable memory and (ii) likely pointer value detection logic coupled to the at least one writable store and responsive to data values destined for register storage of the processor.

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response to final oa dated 12-23-04.doc

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